

Figura 1. Cresterea vitezei de prelucrare intr-o structura pipe-line ideala

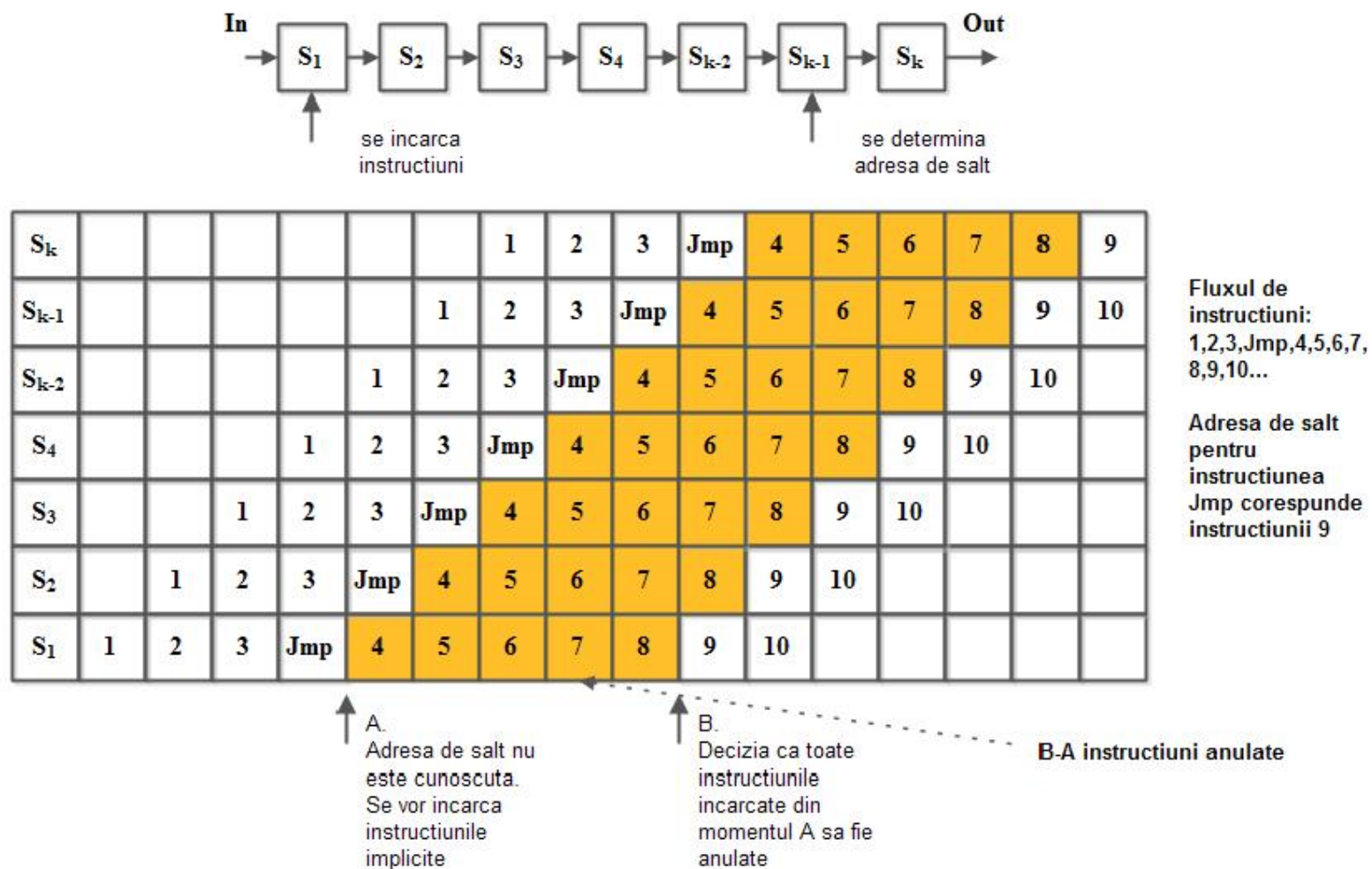


Figura 2. Aparitia instructiunilor de salt intr-o structura pipe-line

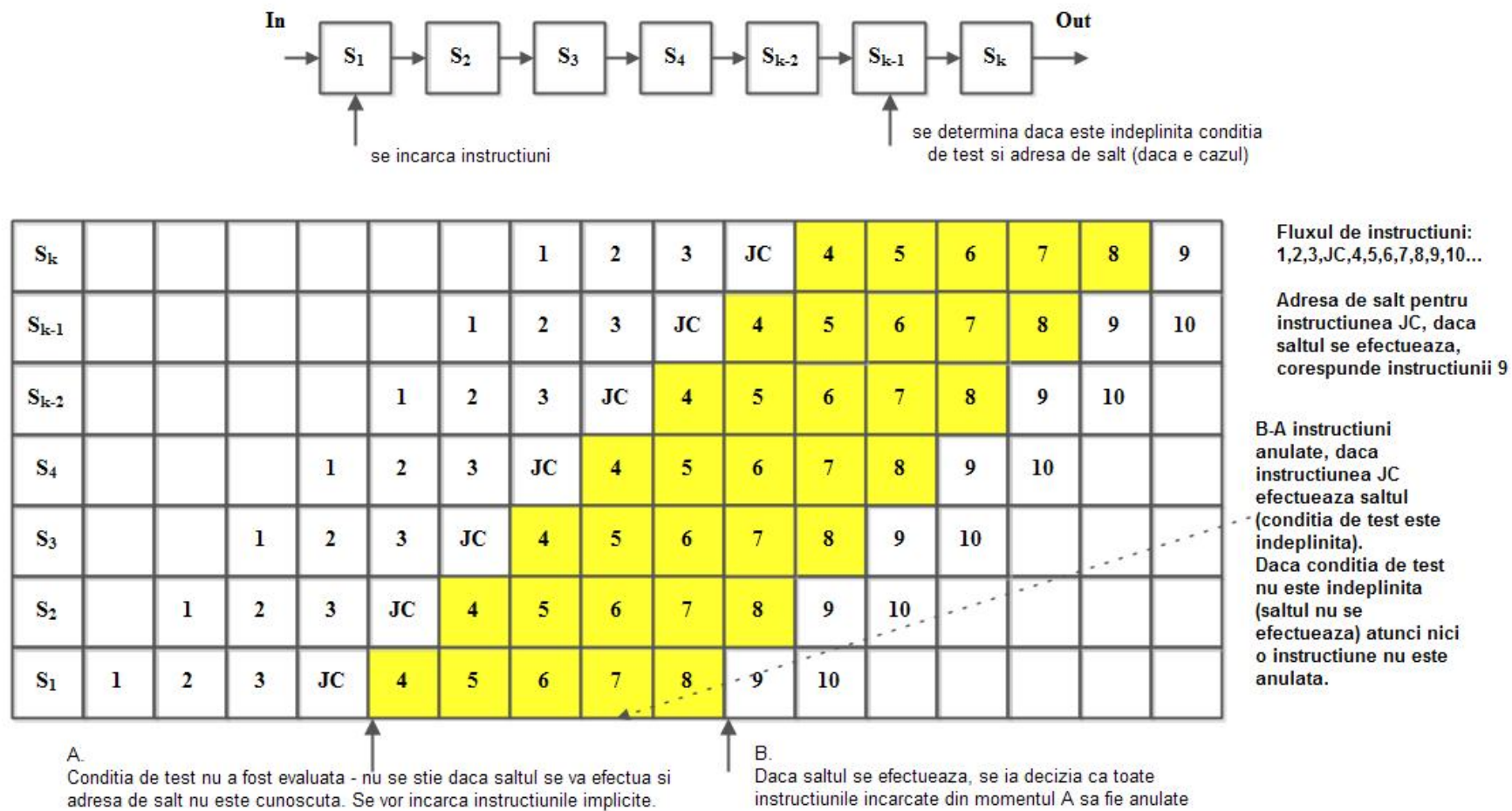


Figura 3. Aparitia instructiunilor de salt conditionat intr-o structura pipe-line

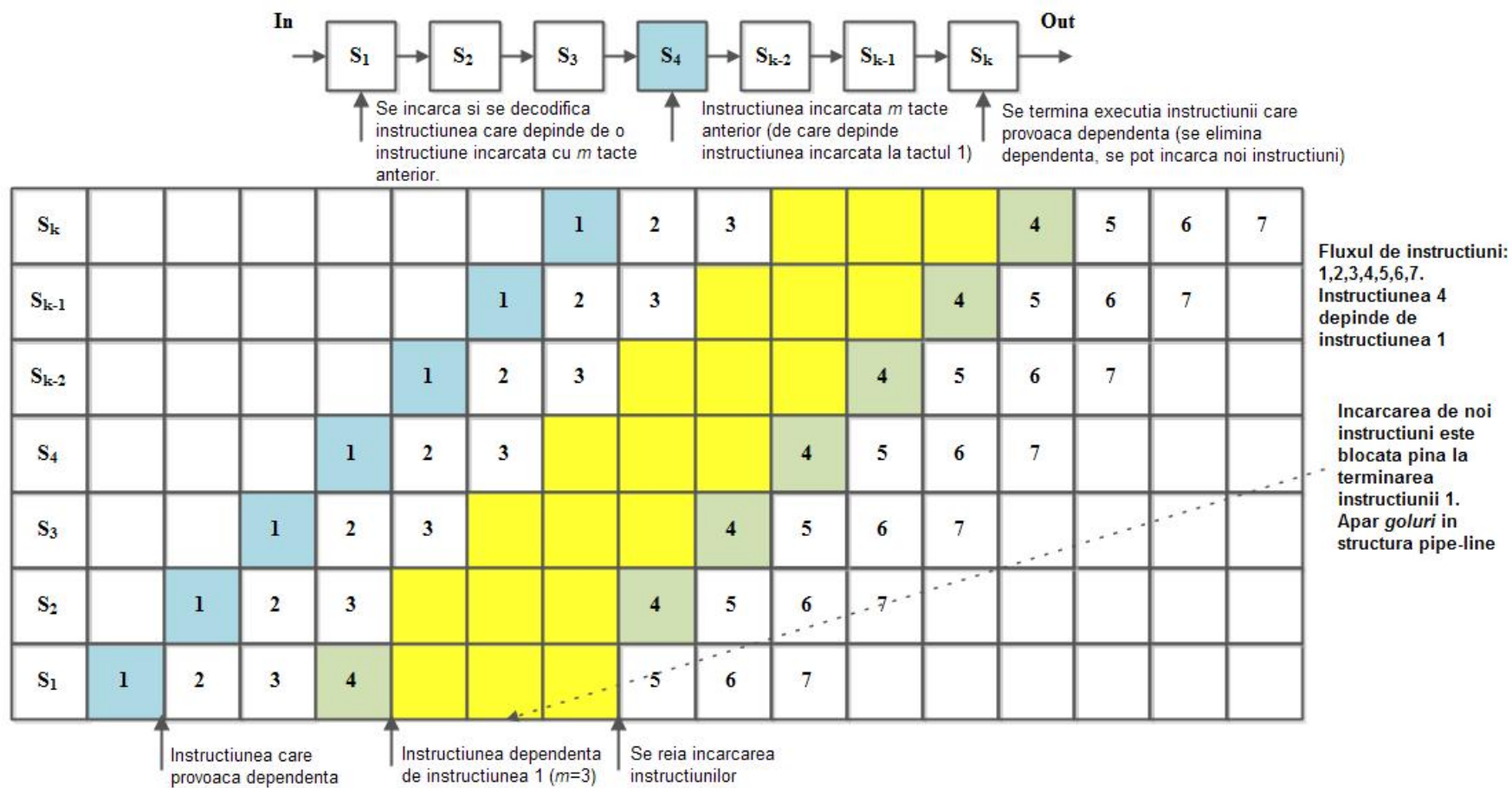


Figura 4. Aparitia instructiunilor dependente intr-o structura pipe-line

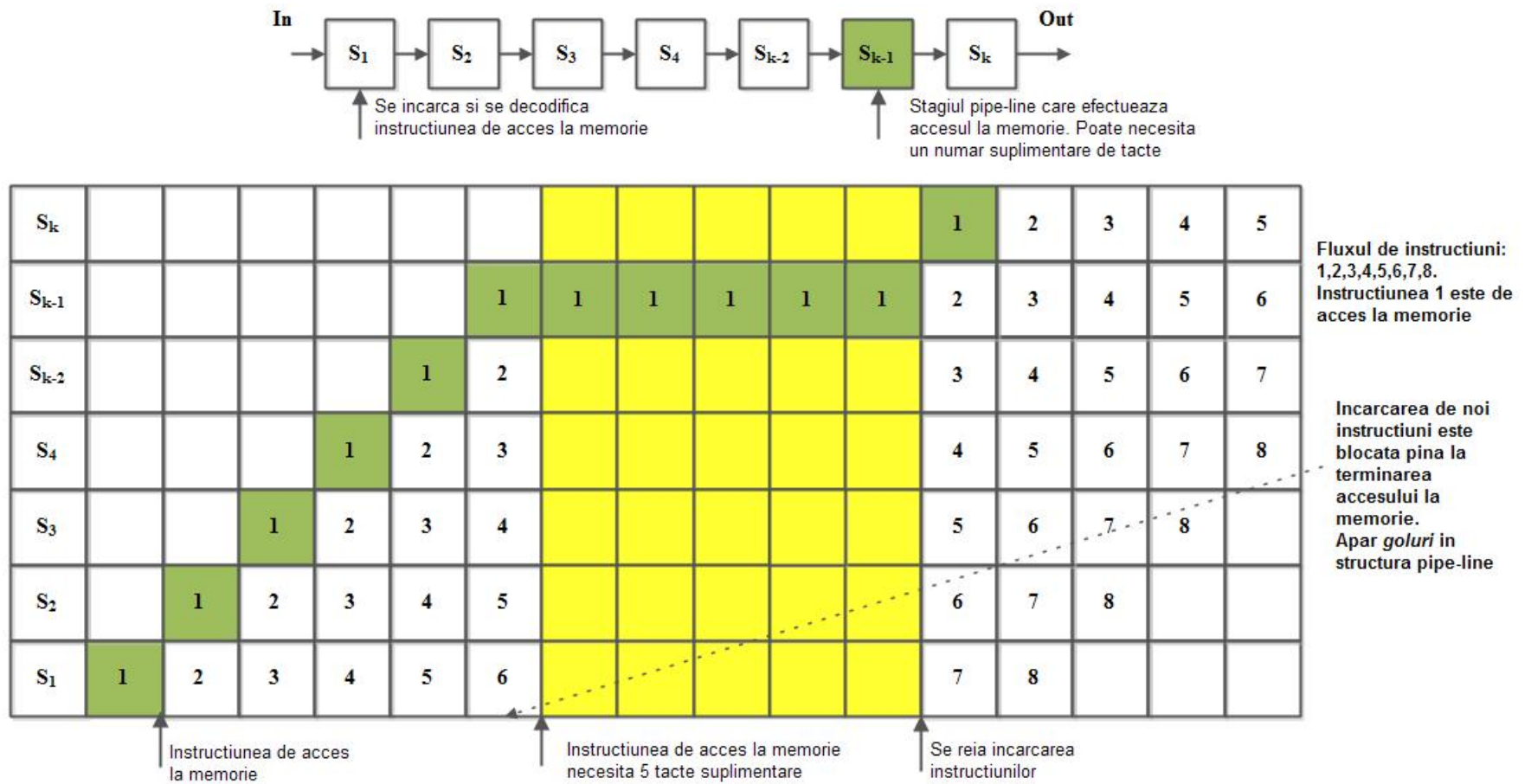


Figura 5. Aparitia instructiunilor de acces la memorie intr-o structura pipe-line

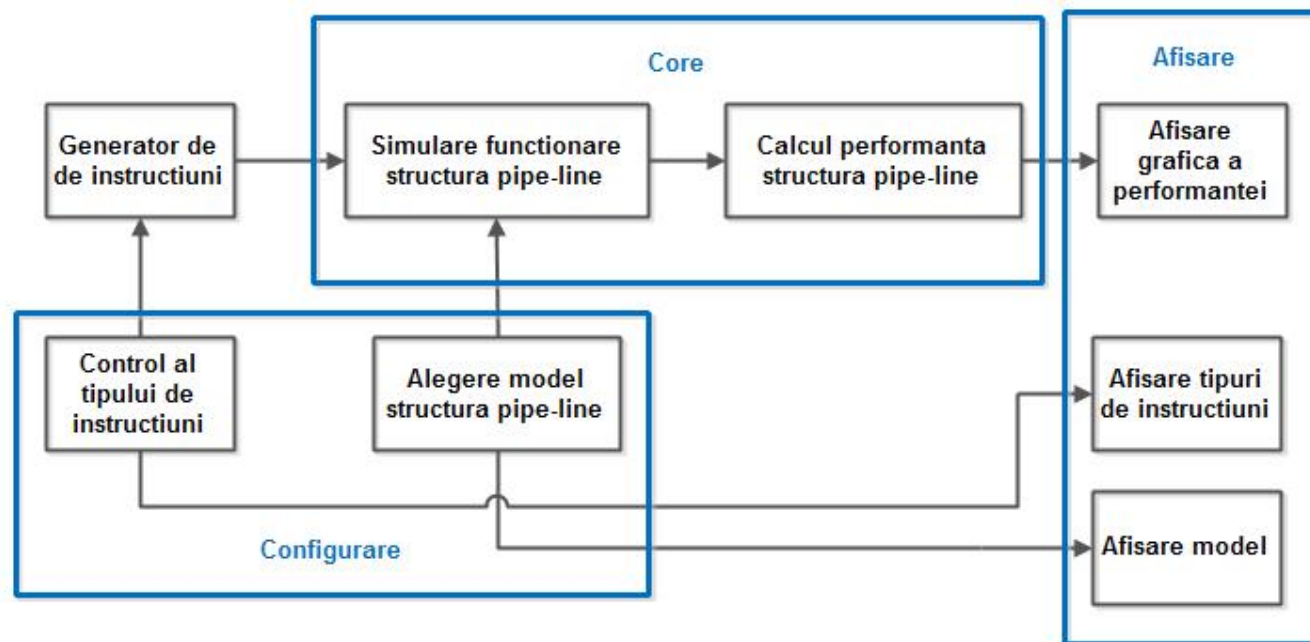


Figura 6. Structura simulatorului **simpl** (SIMulator Pipe-Line)

## MODELUL 1



### Instructiuni simulate:

- salturi neconditionate: JMP
- salturi conditionate neefectuate (not taken): JCondNT
- salturi conditionate efectuate (taken): JCondT

↑  
Decizia de  
efectua a  
salturilor  
conditionate



## MODELUL 2



### Instructiuni simulate:

- salturi neconditionate: JMP
- salturi conditionate neefectuate (not taken): JCondNT
- salturi conditionate efectuate (taken): JCondT

Decizia de  
efectuare a  
salturilor  
conditionate



## MODELUL 3



**Instructiuni simulate:**

- salturi neconditionate: **JMP**
- salturi conditionate neefectuate (not taken): **JCondNT**
- salturi conditionate efectuate (taken): **JCondT**

↑  
**Decizia de  
efectuaire a  
salturilor  
conditionate**

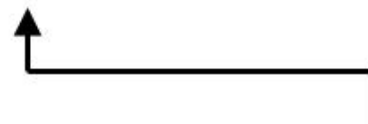
## MODELUL 4



### Instructiuni simulate:

- salturi neconditionate: **JMP**
- salturi conditionate neefectuate (not taken): **JCondNT**
- salturi conditionate efectuate (taken): **JCondT**

Decizia de  
efectuaire a  
salturilor  
conditionate



## MODELUL 5



### Instructiuni simulate:

- salturi neconditionate: JMP
- salturi conditionate neefectuate (not taken): JCondNT
- salturi conditionate efectuate (taken): JCondT
- acces la memorie
- instructiuni dependente

Decizia de efectuare a salturilor conditionate

Bloc lent daca instructiunea este de acces la memorie

Dependenta eliminata

← timp de acces la memorie >> tactul pipe-line

← timp de asteptare a executiei instructiunii care produce dependenta

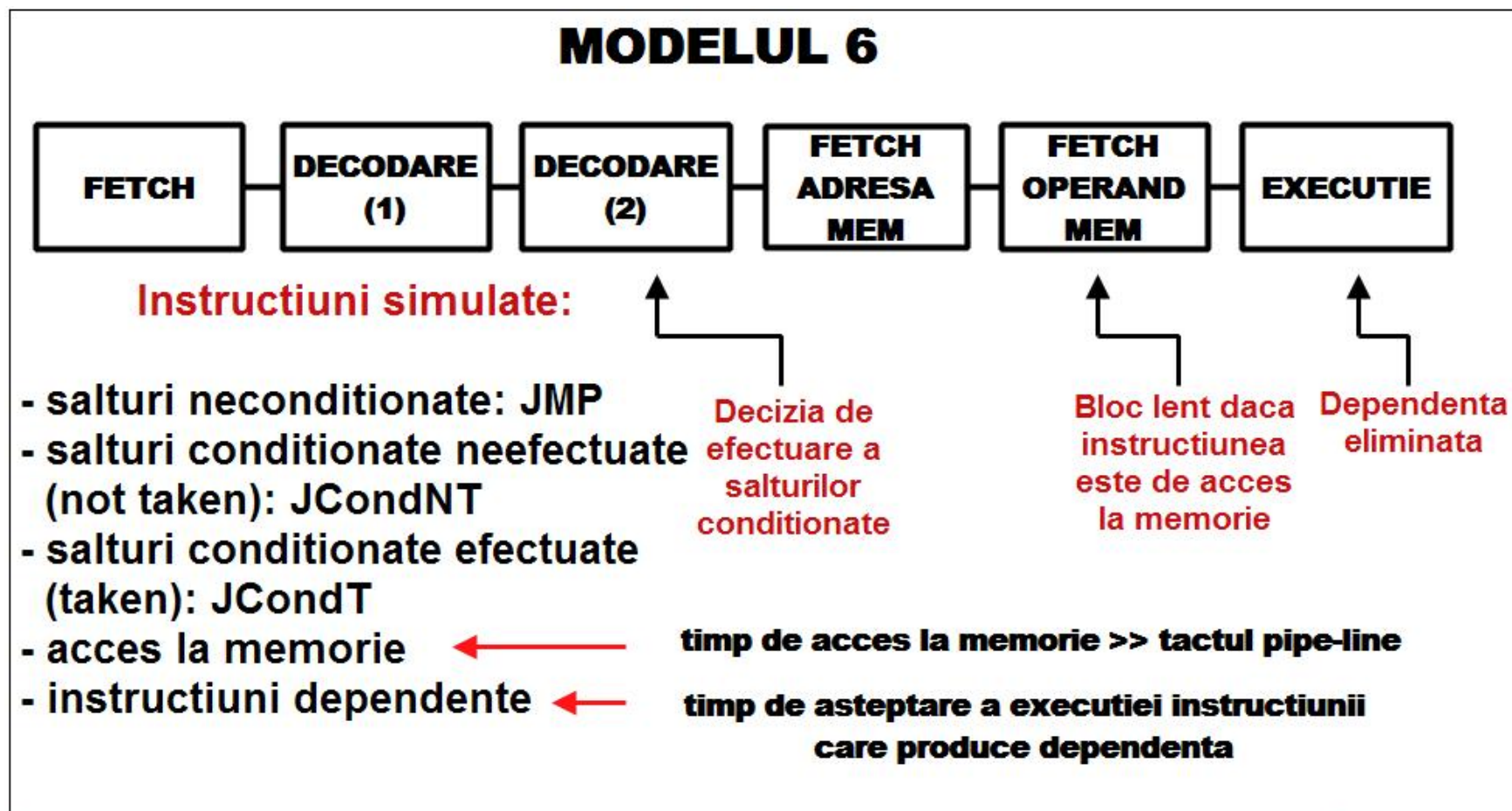


Figura 7. Modelele structurilor pipe-line simulate in simulatorul **simpl**

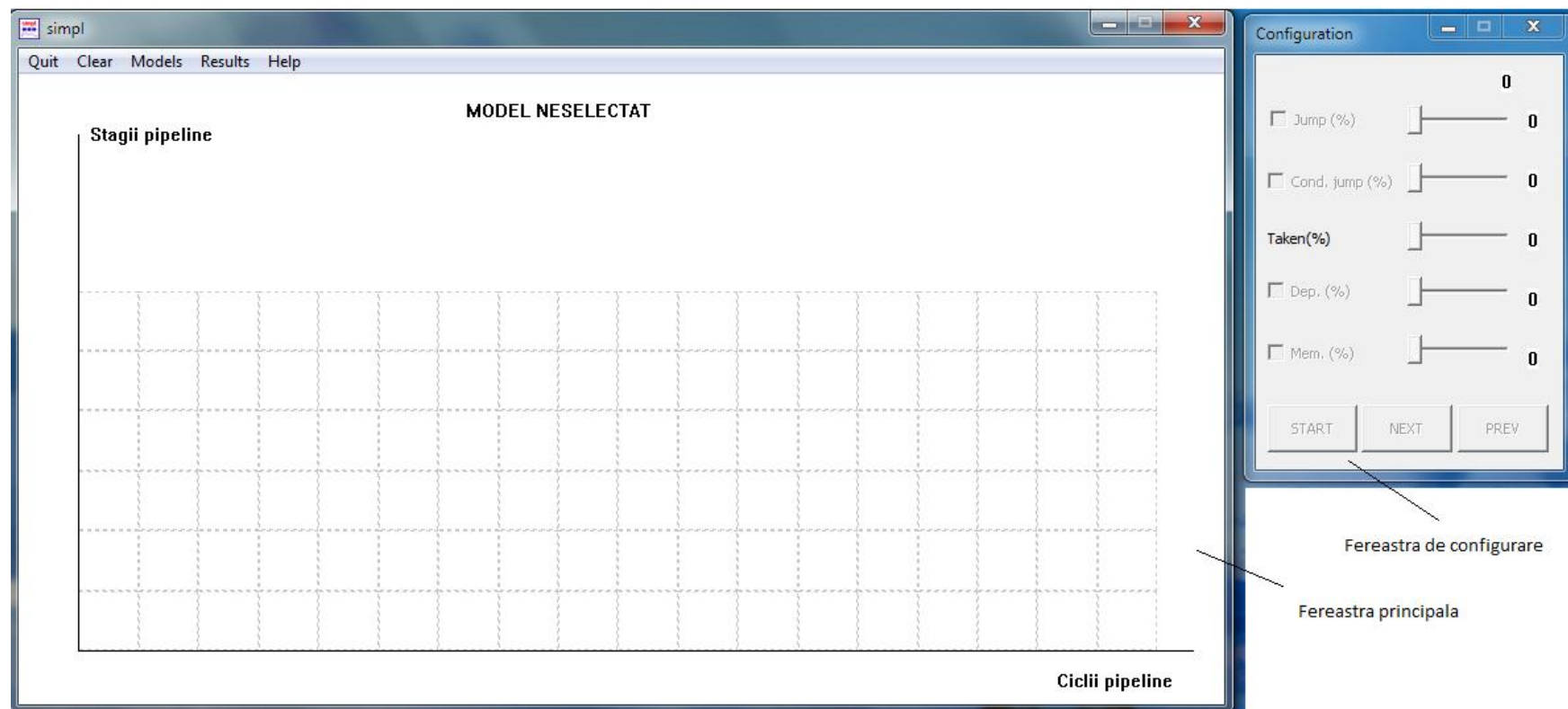


Figura 8. Ferestrele afisate dupa lansarea programului **simpl**

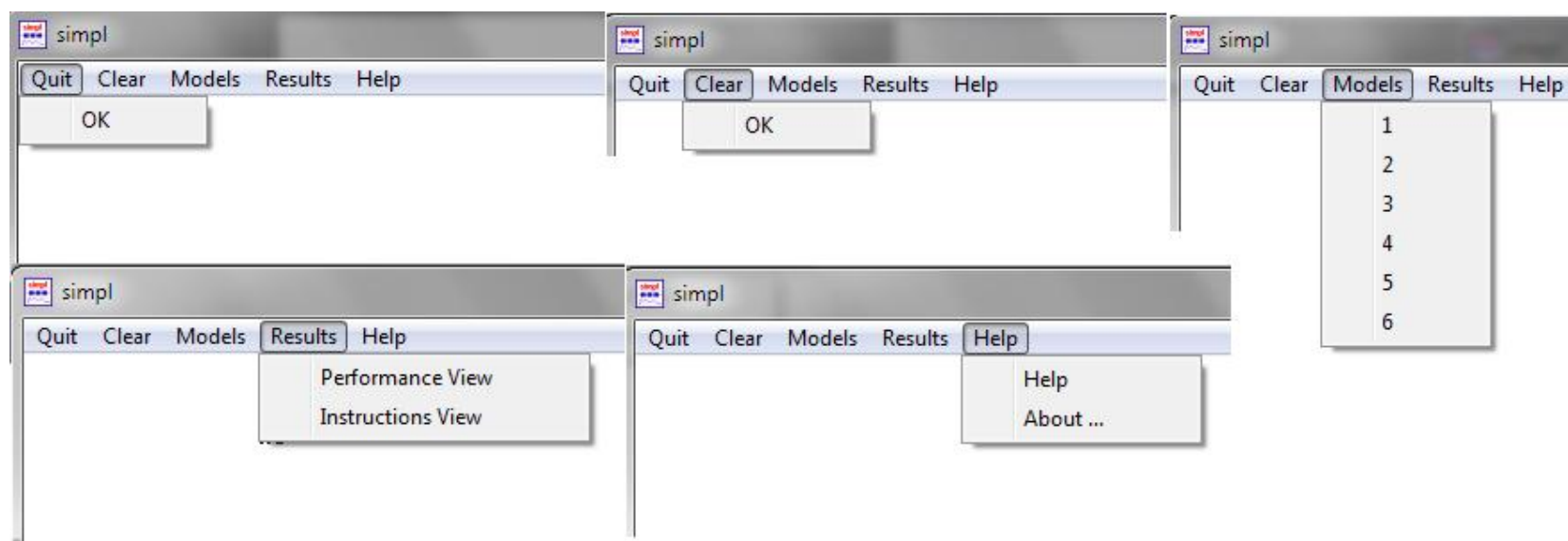



Figura 9. Meniurile ferestrei principale a programului **simpl** 



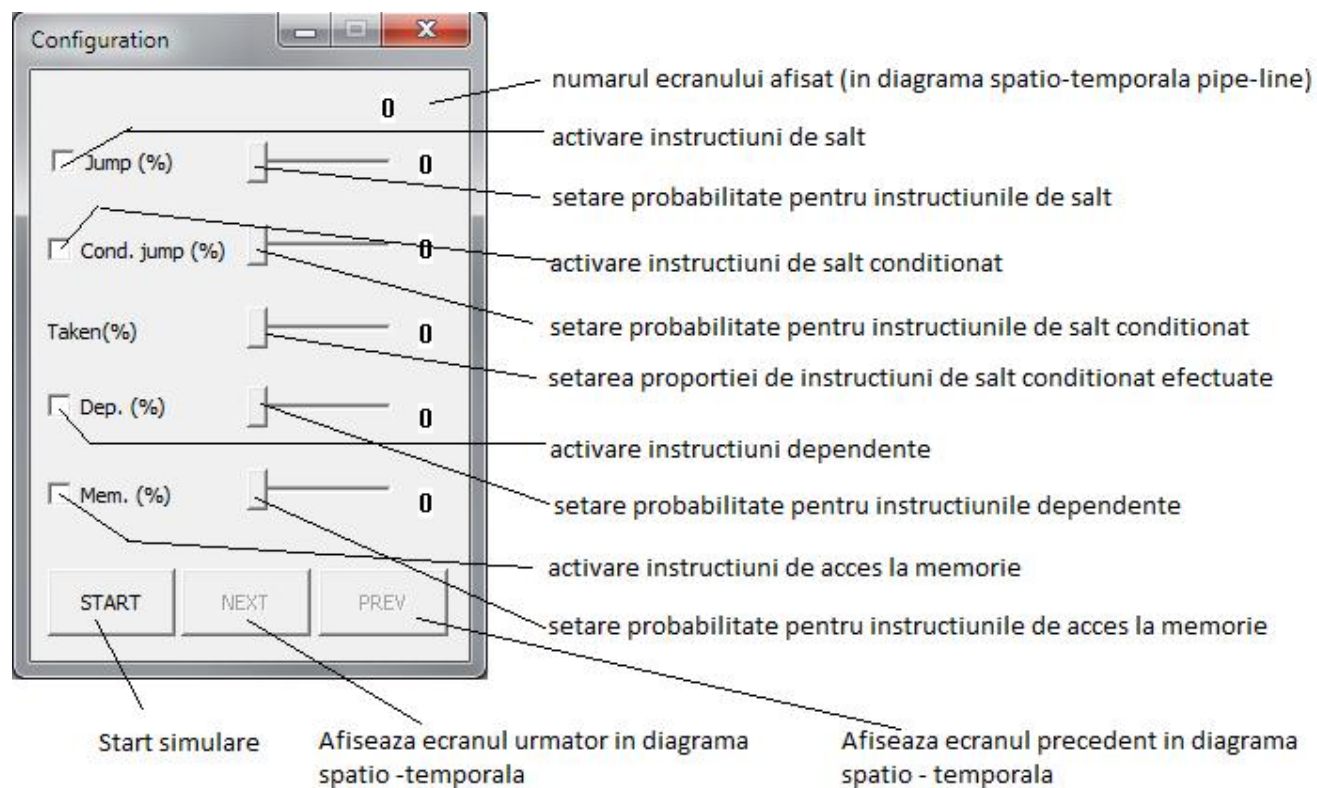



Figura 10. Meniurile ferestrei de configurare a programului **simpl** 



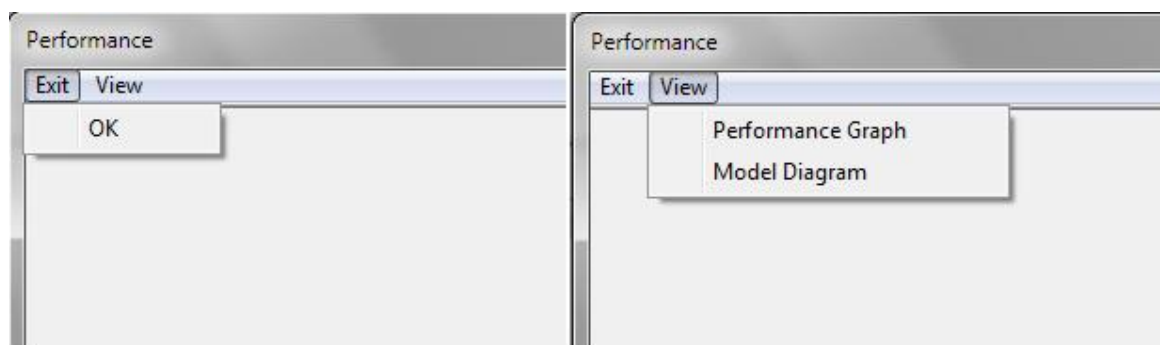



Figura 11. Meniurile ferestrei de afisare a performantelor a programului **simpl** 

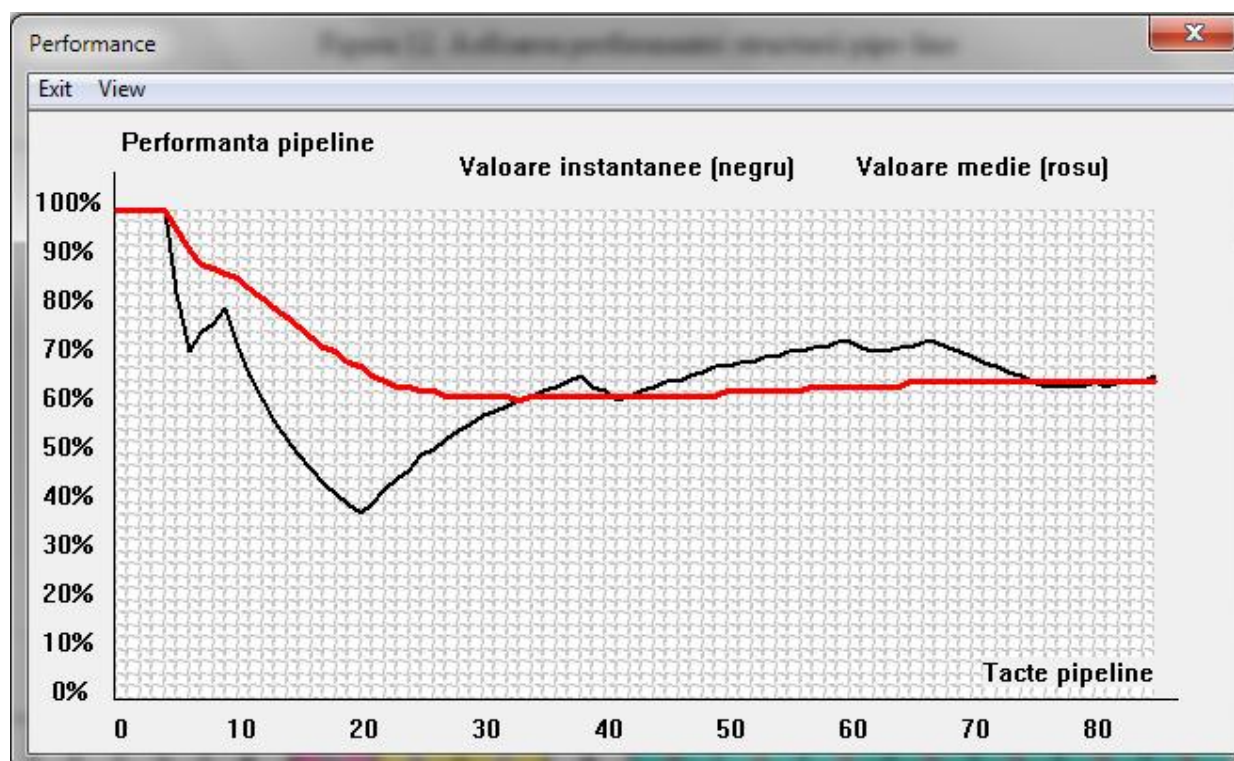


Figura 12. Afisarea performantei structurii pipe-line

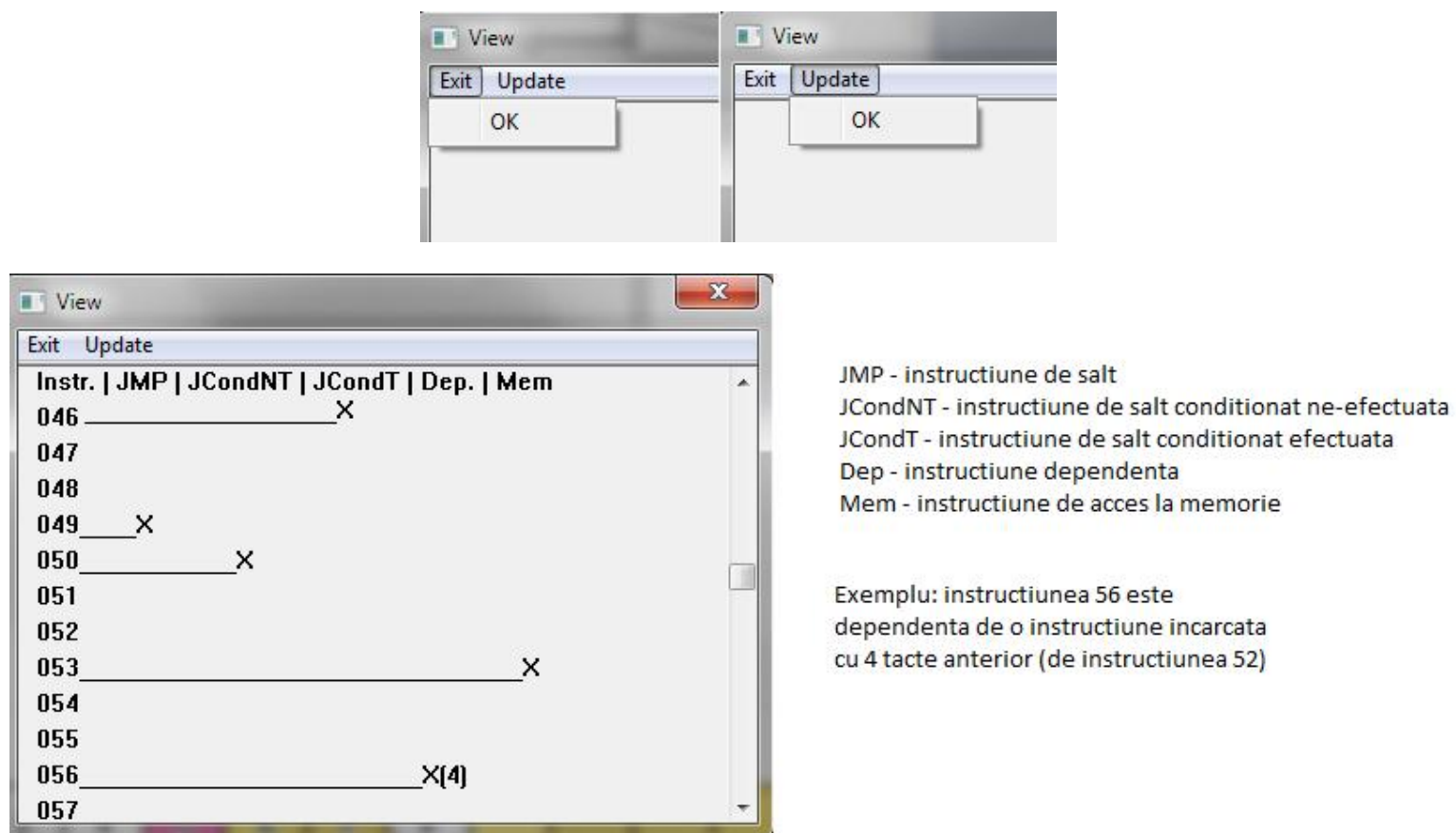

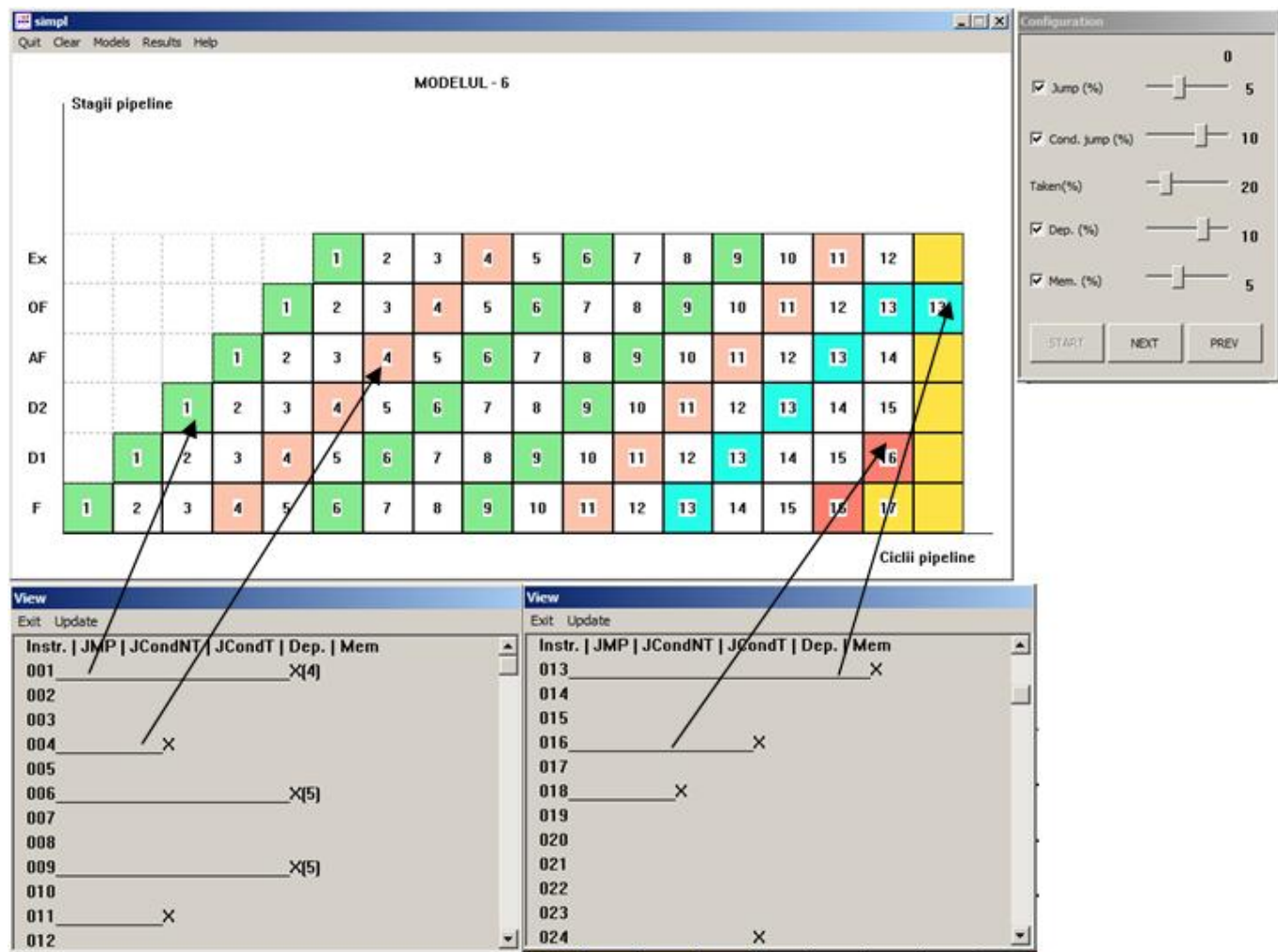


Figura 13. Meniurile ferestrei de afisare a instructiunilor in programului **simpl** 



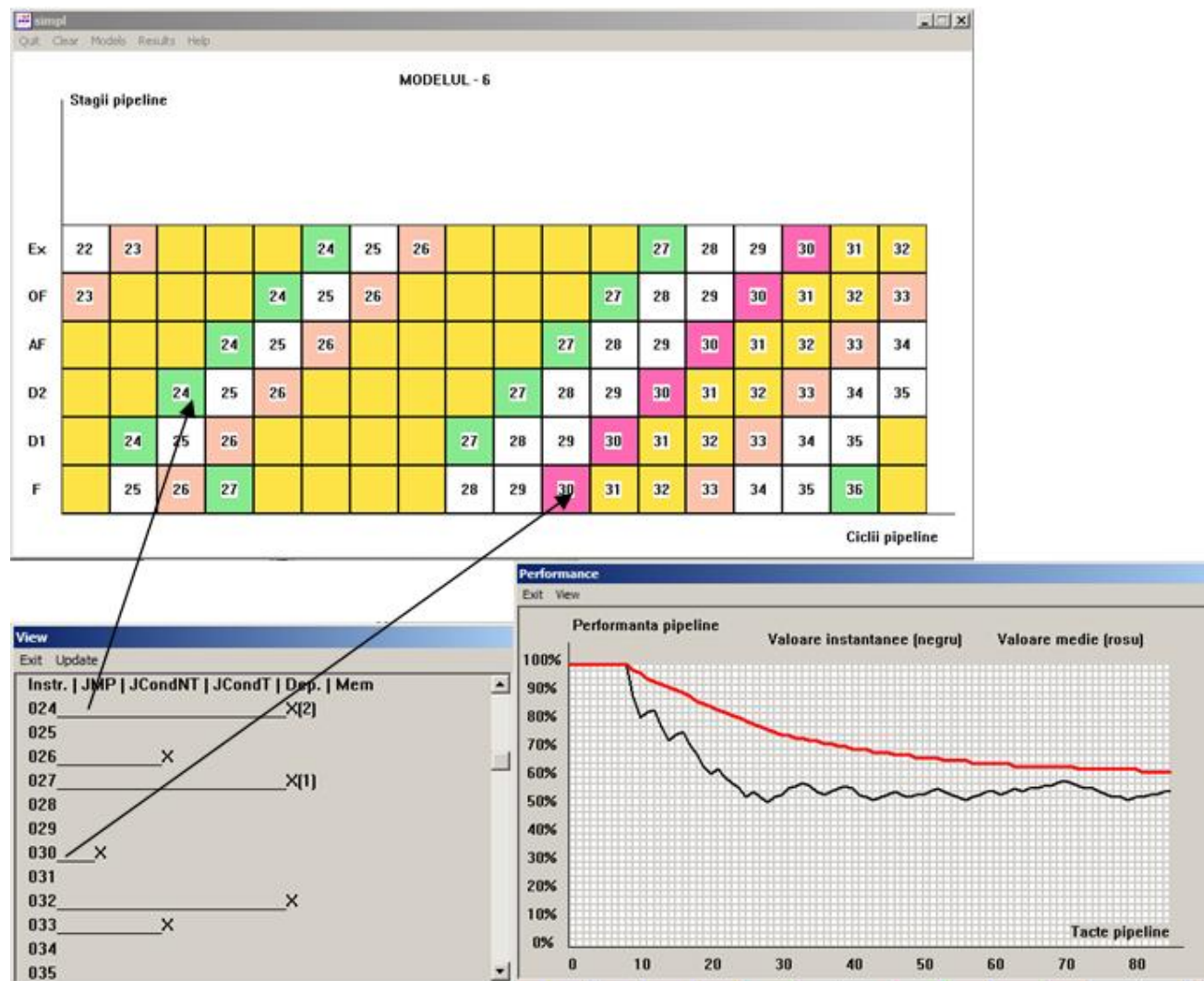


Figura 14. Exemplificarea utilizarii programului **simpl**