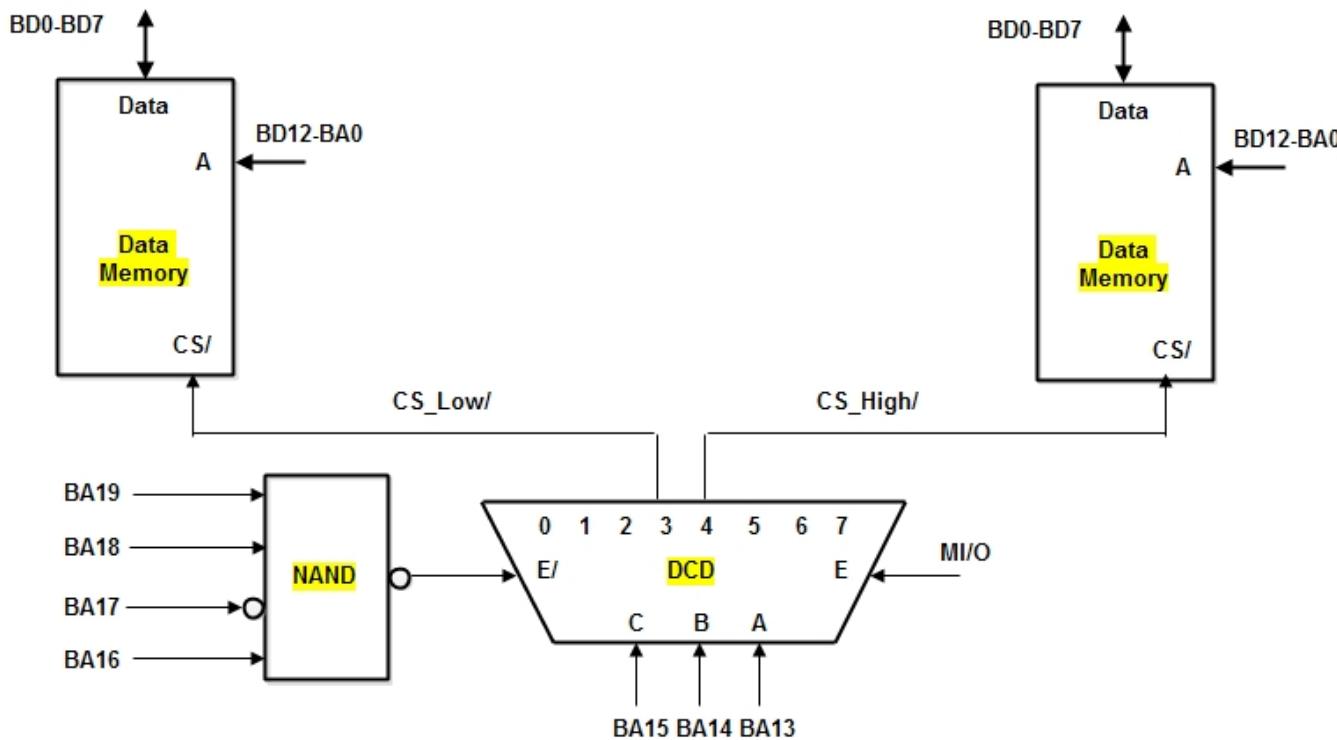


Schema de principiu a placii RK2105

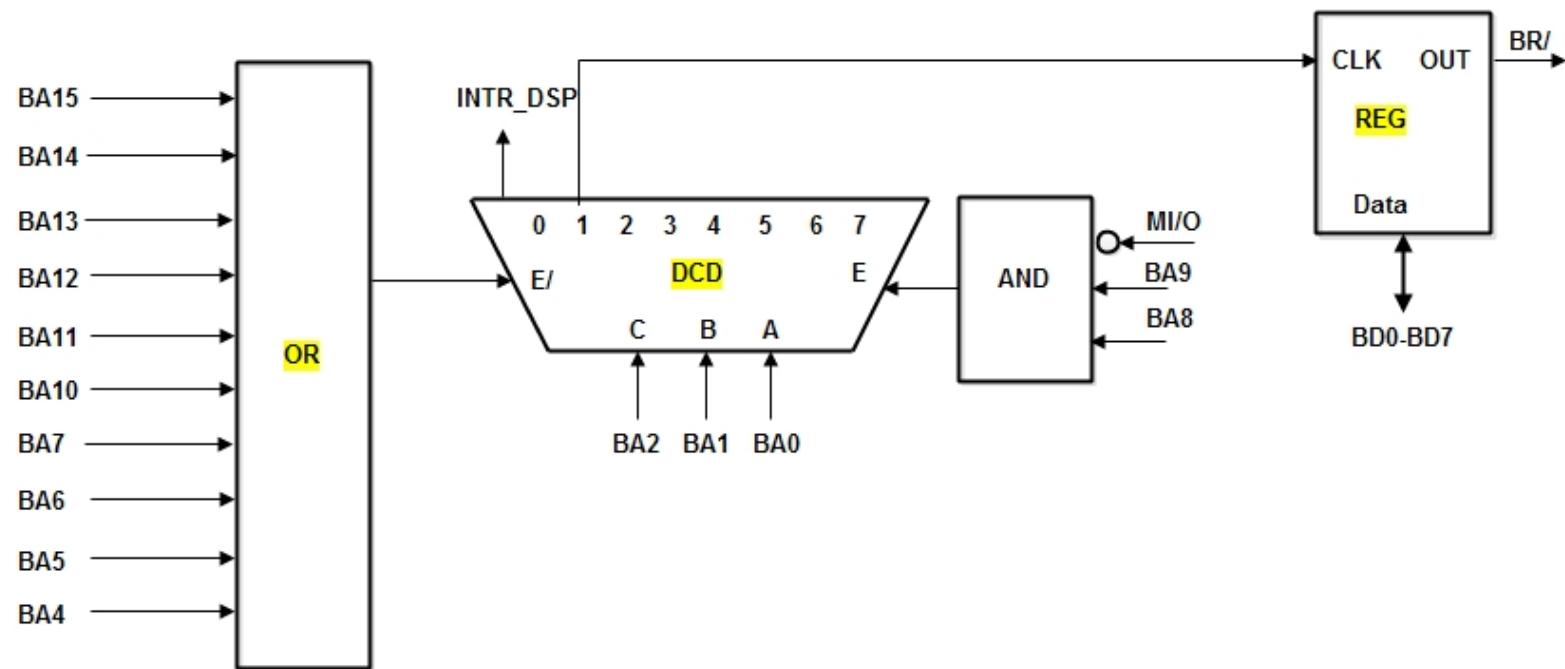
Low Memory (BD0-BD7)	High Memory (BD0-BD7)
BA19 BA18 BA17 BA16 BA15 BA14 BA13 BA12 BA11 ... BA1 BA0 1 1 0 1 0 1 1 0 0 ... 0 0 1 1 0 1 0 1 1 1 1 ... 1 1	BA19 BA18 BA17 BA16 BA15 BA14 BA13 BA12 BA11 ... BA1 BA0 1 1 0 1 1 0 0 0 0 ... 0 0 1 1 0 1 1 0 0 1 1 ... 1 1
0xD6000 0xD7FFF	0xD8000 0xD9FFF



Schema blocului Control Logic (CPU) – selectia memoriei

### IO Port

BA15	BA14	BA13	BA12	BA11	BA10	BA9	BA8	BA7	BA6	BA5	BA4	BA3	BA2	BA1	BA0	
0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0x0300
0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	1	0x0301



Schema blocului Control Logic (CPU) – selectia IO